LTC2054/LTC2055



FEATURES

- Supply Current 150µA (Max per Amplifier) Guaranteed from -40°C to 85°C
- Offset Voltage 3µV (Max, -40°C to 85°C)
- Offset Voltage Drift 30nV/°C (Max, -40°C to 85°C)
- Guaranteed Specifications from –55°C to 150°C (LTC2054MP)
- Common Mode Input Range from V⁻ to V⁺ 0.5V
- Output Swings Rail-to-Rail
- Voltage Gain: 140dB (Typ)
- PSRR and CMRR: 130dB (Typ)
- Input Bias Current: 1pA (Typ, 25°C)
- Noise: 1.6µV_{P-P} (0.01Hz to 10Hz Typ)
- Supply Operation: 2.7V to 6V (LTC2054/LTC2055) 2.7V to ±5.5V (LTC2054HV/LTC2055HV)
- Low Profile (1mm) TSOT-23, MS8 and 3mm × 3mm × 0.8mm DFN Packages

APPLICATIONS

- Thermocouple Amplifiers
- Electronic Scales
- Medical Instrumentation
- Strain Gauge Amplifiers
- High Resolution Data Acquisition
- DC Accurate RC Active Filters
- Low Side Current Sense
- Battery-Powered Systems

Single/Dual Micropower Zero-Drift Operational Amplifiers **DESCRIPTION**

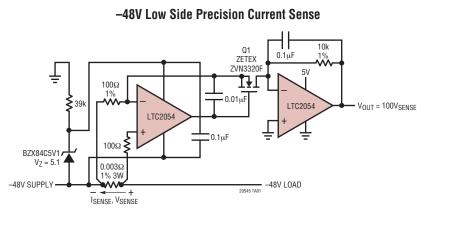
The LTC[®]2054/LTC2055 are low power, low noise, single/ dual, zero-drift operational amplifiers with an extended temperature version (LTC2054MP) that has guaranteed specifications from -55°C to 150°C. They are available in TSOT-23 (ThinSOT^M) and MS8 packages. For space limited applications, the LTC2055 is also available in a 3mm × 3mm × 0.8mm dual fine pitch leadless package (DFN). They operate from a single 2.7V minimum supply and support ±5V applications. The current consumption is typically 150µA for the LTC2054 and 130µA/amp for the LTC2055.

The LTC2054/LTC2055, despite their miniature size, feature uncompromising DC performance. The maximum input offset voltage and offset drift are 3.0μ V and 30nV/°C over the -40°C to 85°C industrial temperature range. The almost zero DC offset and drift are supported with a power supply rejection ratio (PSRR) and common mode rejection ratio (CMRR) of 130dB (typ).

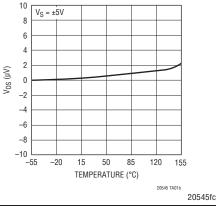
The input common mode voltage ranges from the negative supply up to typically 0.5V below the positive supply. The open-loop gain is typically 140dB. The LTC2054/LTC2055 also feature a $1.6\mu V_{P-P}$ DC to 10Hz noise and a 500kHz gain-bandwidth product.

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TYPICAL APPLICATION



V_{OS} vs Temperature



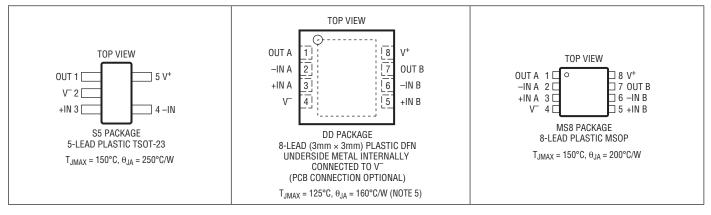


ABSOLUTE MAXIMUM RATINGS (Note 1)

Total Supply Voltage (V ⁺ to V ⁻)	
LTC2054/LTC2055	7V
LTC2054HV/LTC2055HV	.12V
Input Voltage $(V^+ + 0.3V)$ to $(V_S^ 0.000)$	
Input Current±1	
Output Short-Circuit Duration Inde	finite

LTC2054C/LTC2055C	0°C to 70°C
LTC2054I/LTC2055I	40°C to 85°C
LTC2054H/LTC2055H	40°C to 125°C
LTC2054MP	–55°C to 150°C
Storage Temperature Range	65°C to 150°C
DD Package	–65°C to 125°C
Lead Temperature (Soldering, 10 sec)	
TSOT23 and MS8 Packages	

PIN CONFIGURATION



ORDER INFORMATION

LEAD FREE FINISH	TAPE AND REEL	PART MARKING*	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE
LTC2054CS5#PBF	LTC2054CS5#TRPBF	LTAGB	5-Lead Plastic TSOT-23	0°C to 70°C
LTC2054HVCS5#PBF	LTC2054HVCS5#TRPBF	LTAGD	5-Lead Plastic TSOT-23	0°C to 70°C
LTC2054IS5#PBF	LTC2054IS5#TRPBF	LTAGB	5-Lead Plastic TSOT-23	-40°C to 85°C
LTC2054HVIS5#PBF	LTC2054HVIS5#TRPBF	LTAGD	5-Lead Plastic TSOT-23	-40°C to 85°C
LTC2054HS5#PBF	LTC2054HS5#TRPBF	LTAGB	5-Lead Plastic TSOT-23	-40°C to 125°C
LTC2054HVHS5#PBF	LTC2054HVHS5#TRPBF	LTAGD	5-Lead Plastic TSOT-23	-40°C to 125°C
LTC2055CDD#PBF	LTC2055CDD#TRPBF	LBCW	8-Lead (3mm × 3mm) Plastic DFN	0°C to 70°C
LTC2055HVCDD#PBF	LTC2055HVCDD#TRPBF	LBCX	8-Lead (3mm × 3mm) Plastic DFN	0°C to 70°C
LTC2055IDD#PBF	LTC2055IDD#TRPBF	LBCW	8-Lead (3mm × 3mm) Plastic DFN	-40°C to 85°C
LTC2055HVIDD#PBF	LTC2055HVIDD#TRPBF	LBCX	8-Lead (3mm × 3mm) Plastic DFN	-40°C to 85°C
LTC2055HDD#PBF	LTC2055HDD#TRPBF	LBCW	8-Lead (3mm × 3mm) Plastic DFN	-40°C to 125°C
LTC2055HVHDD#PBF	LTC2055HVHDD#TRPBF	LBCX	8-Lead (3mm × 3mm) Plastic DFN	-40°C to 125°C
LTC2055CMS8#PBF	LTC2054CMS8#TRPBF	LTBCR	8-Lead Plastic MSOP	0°C to 70°C
LTC2055HVCMS8#PBF	LTC2055HVCMS8#TRPBF	LTBCT	8-Lead Plastic MSOP	0°C to 70°C
LTC2055IMS8#PBF	LTC2055IMS8#TRPBF	LTBCR	8-Lead Plastic MSOP	-40°C to 85°C
				20545



ORDER INFORMATION

LEAD FREE FINISH	TAPE AND REEL	PART MARKING*	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE
LTC2055HVIMS8#PBF	LTC2055HVIMS8#TRPBF	LTBCT	8-Lead Plastic MSOP	-40°C to 85°C
LTC2055HMS8#PBF	LTC2055HMS8#TRPBF	LTBCR	8-Lead Plastic MSOP	-40°C to 125°C
LTC2055HVHMS8#PBF	LTC2055HVHMS8#TRPBF	LTBCT	8-Lead Plastic MSOP	-40°C to 125°C
LTC2054MPS5#PBF	LTC2054MPS5#TRPBF	LTFFF	5-Lead Plastic TSOT-23	-55°C to 150°C
LTC2054HVMPS5#PBF	LTC2054HVMPS5#TRPBF	LTFFG	5-Lead Plastic TSOT-23	-55°C to 150°C

Consult LTC Marketing for parts specified with wider operating temperature ranges. *The temperature grade is identified by a label on the shipping container. Consult LTC Marketing for information on non-standard lead based finish parts.

For more information on lead free part marking, go to: http://www.linear.com/leadfree/ For more information on tape and reel specifications, go to: http://www.linear.com/tapeandreel/

ELECTRICAL CHARACTERISTICS (LTC2054/LTC2055) The \bullet denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_S = 3V, 5V unless otherwise noted. (Note 2)

)54C/LTC2 054I/LTC2		LTC2	054H/LTC	2055H	
SYMBOL	PARAMETER	CONDITIONS		MIN	ТҮР	MAX	MIN	TYP	MAX	UNITS
I _S	Supply Current (LTC2054)	No Load	٠		140	175		140	180	μA
I _S	Supply Current Per Amplifier (LTC2055)	No Load	•		130	150		130	155	μA
V _{OS}	Input Offset Voltage	(Note 3)			±0.5	±3		±0.5	±3	μV
$\Delta V_{0S} / \Delta T$	Average Input Offset Drift	(Note 3)	•		0.02	±0.03		0.02	±0.05	μV/°C
	Long-Term Offset Drift				50			50		nV/√mo
I _B	Input Bias Current	(Note 4)	•		±1	±150		±1	±3000	pA pA
I _{OS}	Input Offset Current	(Note 4)	•		±2	±300		±2	±700	pA pA
e _n	Input Noise Voltage	R_S = 100 Ω , DC to 1Hz R_S = 100 Ω , DC to 10Hz			0.6 1.6			0.6 1.6		μV _{P-P} μV _{P-P}
CMRR	Common Mode Rejection Ratio	$V_{CM} = GND$ to V ⁺ – 0.7V V _S = 3V	•	115 110	130		115 110	130		dB dB
		$V_{CM} = GND$ to V ⁺ – 0.7V V _S = 5V	•	120 115	130		120 115	130		dB dB
PSRR	Power Supply Rejection Ratio	V _S = 2.7V to 6V	•	120 115	130		120 115	130		dB dB
A _{VOL}	Large-Signal Voltage Gain	$R_L = 100k, V_S = 3V, V_{OUT} = V_S/2$	•	120 115	135		120 115	135		dB dB
		$R_{L} = 100k, V_{S} = 5V, V_{OUT} = V_{S}/2$	•	125 120	140		125 120	140		dB dB
V _{OUT}	Output Voltage Swing High	$ \begin{array}{l} R_{L} = 5k \text{ to GND}, \ V_{S} = 3V \\ R_{L} = 5k \text{ to GND}, \ V_{S} = 3V \end{array} $	•	2.87 2.85	2.89		2.87 2.84	2.89		V V
		$ \begin{array}{l} R_L = 5k \text{ to GND}, \ V_S = 5V \\ R_L = 5k \text{ to GND}, \ V_S = 5V \end{array} $	•	4.80 4.75	4.83		4.80 4.70	4.83		V V
		$ \begin{array}{l} R_L = 100k \mbox{ to GND}, V_S = 3V \\ R_L = 100k \mbox{ to GND}, V_S = 3V \end{array} $	•	2.98 2.975	2.99		2.98 2.97	2.99		V V
		$ \begin{array}{l} R_L = 100k \mbox{ to GND}, V_S = 5V \\ R_L = 100k \mbox{ to GND}, V_S = 5V \end{array} $	•	4.985 4.980	4.99		4.985 4.970	4.99		V V



ELECTRICAL CHARACTERISTICS (LTC2054/LTC2055) The \bullet denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_S = 3V, 5V unless otherwise noted. (Note 2)

					054C/LTC 054I/LTC		LTC2	054H/LTC	2055H	
SYMBOL	PARAMETER	CONDITIONS		MIN	ТҮР	MAX	MIN	ТҮР	MAX	UNITS
V _{OUT}	Output Voltage Swing Low	$ \begin{array}{l} R_{L} = 5k \text{ to GND}, \ V_{S} = 3V \\ R_{L} = 5k \text{ to GND}, \ V_{S} = 3V \end{array} $	•		2	8 10		3	8 10	mV mV
		$ \begin{array}{l} R_{L} = 5k \text{ to GND}, \ V_{S} = 5V \\ R_{L} = 5k \text{ to GND}, \ V_{S} = 5V \end{array} $	•		2	8 10		3	8 10	mV mV
		$ \begin{array}{l} R_L = 100 \text{k to GND}, \ V_S = 3V \\ R_L = 100 \text{k to GND}, \ V_S = 3V \end{array} $	•		2	8 10		3	8 10	mV mV
		$ \begin{array}{l} R_L = 100k \mbox{ to GND}, V_S = 5V \\ R_L = 100k \mbox{ to GND}, V_S = 5V \end{array} $	•		2	8 10		3	8 10	mV mV
SR	Slew Rate				0.5			0.5		V/µs
GBW	Gain Bandwidth Product				500			500		kHz
f _S	Internal Sampling Frequency				1			1		kHz

(LTC2054HV/LTC2055HV) The \bullet denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^{\circ}$ C. $V_S = \pm 5$ V unless otherwise noted. (Note 2)

					HVC/LTC2 4HVI/LTC2		LTC205	4HVH/LTC	2055HVH	
SYMBOL	PARAMETER	CONDITIONS		MIN	ТҮР	MAX	MIN	ТҮР	MAX	UNITS
I _S	Supply Current	No Load (LTC2054)	•		175	210		175	215	μA
I _S	Supply Current (Per Amplifier)	No Load (LTC2055)			150	180		150	185	μA
V _{OS}	Input Offset Voltage	(Note 3)			±0.5	±5		±0.5	±5	μV
$\Delta V_{0S} / \Delta T$	Average Input Offset Drift	(Note 3)	•		0.025	±0.03		0.025	±0.05	μV/°C
	Long-Term Offset Drift				50			50		nV/√mo
IB	Input Bias Current	(Note 4)	•		±3	±150		±3	±3000	рА рА
I _{OS}	Input Offset Current	(Note 4)	•		±6	±300		±6	±700	рА рА
e _n	Input Noise Voltage	$\begin{array}{l} R_S = 100\Omega, \ DC \ to \ 1Hz \\ R_S = 100\Omega, \ DC \ to \ 1OHz \end{array}$			0.6 1.6			0.6 1.6		μV _{P-P} μV _{P-P}
CMRR	Common Mode Rejection Ratio	$V_{CM} = GND$ to $V^+ - 0.9$	•	120 115	130		120 115	130		dB dB
PSRR	Power Supply Rejection Ratio	V _S = 2.7V to 11V	•	120 115	130		120 115	130		dB dB
AVOL	Large-Signal Voltage Gain	R _L = 100k, V _{OUT} = GND	•	125 120	140		125 120	140		dB dB
V _{OUT}	Maximum Output Voltage Swing	R _L = 5k to GND R _L = 5k to GND	•	±4.78 ±4.75	±4.82		±4.78 ±4.70	±4.82		V V
		R _L = 100k to GND R _L = 100k to GND	•	±4.98 ±4.975	±4.99		±4.98 ±4.97	±4.99		V V
SR	Slew Rate				0.5			0.5		V/µs
GBW	Gain Bandwidth Product				500			500		kHz
f _S	Internal Sampling Frequency				1			1		kHz



ELECTRICAL CHARACTERISTICS (LTC2054MP) The \bullet denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_S = 3V, 5V unless otherwise noted. (Note 2)

SYMBOL	PARAMETER	CONDITIONS		MIN	ТҮР	MAX	UNITS
Is	Supply Current	No Load	•		145	190	μA
V _{OS}	Input Offset Voltage	(Note 3) (Note 3) –55°C to 135°C	•			±10 ±8	μV μV
$\Delta V_{0S} / \Delta T$	Average Input Offset Drift	(Note 3)	•		0.04	±0.1	μV/°C
	Long-Term Offset Drift				50		nV/√mo
I _B	Input Bias Current	(Note 4)	•		±1	±12	pA nA
I _{OS}	Input Offset Current	(Note 4)	•		±2	±5	pA nA
e _n	Input Noise Voltage	$ \begin{array}{l} R_{S} = 100\Omega, DC \text{ to } 1Hz \\ R_{S} = 100\Omega, DC \text{ to } 1OHz \end{array} $			0.6 1.6		μV _{P-P} μV _{P-P}
CMRR	Common Mode Rejection Ratio	$V_{CM} = GND$ to V ⁺ - 0.7V V _S = 3V	•	115 105	130		dB dB
		V_{CM} = GND to V ⁺ - 0.7V V _S = 5V	•	120 110	130		dB dB
PSRR	Power Supply Rejection Ratio	V _S = 2.7V to 6V	•	120 110	130		dB dB
A _{VOL}	Large-Signal Voltage Gain	$R_L = 100k, V_S = 3V, V_{OUT} = V_S/2$	•	120 110	135		dB dB
		$R_L = 100k, V_S = 5V, V_{OUT} = V_S/2$	•	125 115	140		dB dB
V _{OUT}	Output Voltage Swing High	$R_L = 5k \text{ to GND}, V_S = 3V$ $R_L = 5k \text{ to GND}, V_S = 3V$	•	2.87 2.84	2.89		V V
		$ \begin{array}{l} R_{L} = 5k \text{ to GND, } V_{S} = 5V \\ R_{L} = 5k \text{ to GND, } V_{S} = 5V \end{array} $	•	4.80 4.70	4.83		V V
		$ \begin{array}{l} R_L = 100k \mbox{ to GND}, V_S = 3V \\ R_L = 100k \mbox{ to GND}, V_S = 3V \end{array} $	•	2.98 2.97	2.99		V V
		$ \begin{array}{l} R_L = 100k \mbox{ to GND}, V_S = 5V \\ R_L = 100k \mbox{ to GND}, V_S = 5V \end{array} $	•	4.985 4.970	4.99		V V
V _{OUT}	Output Voltage Swing Low	$R_L = 5k \text{ to GND}, V_S = 3V$ $R_L = 5k \text{ to GND}, V_S = 3V$	•		3	8 10	mV mV
		$R_L = 5k \text{ to GND}, V_S = 5V$ $R_L = 5k \text{ to GND}, V_S = 5V$	•		3	8 10	mV mV
		$ \begin{array}{l} R_{L} = 100 k \ to \ GND, \ V_{S} = 3V \\ R_{L} = 100 k \ to \ GND, \ V_{S} = 3V \end{array} $	•		3	8 10	mV mV
		$ \begin{array}{l} R_L = 100k \text{ to GND, } V_S = 5V \\ R_L = 100k \text{ to GND, } V_S = 5V \end{array} $	•		3	8 10	mV mV
SR	Slew Rate				0.5		V/µs
GBW	Gain Bandwidth Product				500		kHz
f _S	Internal Sampling Frequency				1		kHz

ELECTRICAL CHARACTERISTICS (LTC2054HVMP) The • denotes the specifications which apply over the full

operating temperature range, otherwise specifications are at $T_A = 25^{\circ}C$. $V_S = \pm 5V$ unless otherwise noted. (Note 2)

SYMBOL	PARAMETER	CONDITIONS		MIN	ТҮР	MAX	UNITS
Is	Supply Current	No Load	•		175	220	μA
V _{OS}	Input Offset Voltage	(Note 3)	•			±10	μV
$\Delta V_{OS} / \Delta T$	Average Input Offset Drift	(Note 3)	•		0.05	±0.1	μV/°C
	Long-Term Offset Drift				50		nV/√mo
I _B	Input Bias Current	(Note 4)	•		±3	±12	pA nA
I _{OS}	Input Offset Current	(Note 4)	•		±6	±5	pA nA
e _n	Input Noise Voltage	R_{S} = 100 $\Omega,$ DC to 1Hz R_{S} = 100 $\Omega,$ DC to 10Hz			0.6 1.6		μV _{P-P} μV _{P-P}
CMRR	Common Mode Rejection Ratio	V_{CM} = GND to V ⁺ - 0.9	•	120 110	130		dB dB
PSRR	Power Supply Rejection Ratio	V _S = 2.7V to 11V	•	120 110	130		dB dB
AVOL	Large-Signal Voltage Gain	$R_L = 100k, V_{OUT} = GND$	•	125 115	140		dB dB
V _{OUT}	Maximum Output Voltage Swing	R _L = 5k to GND R _L = 5k to GND	•	±4.78 ±4.675	±4.82		V V
		$R_L = 100k$ to GND $R_L = 100k$ to GND	•	±4.98 ±4.965	±4.99		V V
SR	Slew Rate				0.5		V/µs
GBW	Gain Bandwidth Product				500		kHz
f _S	Internal Sampling Frequency				1		kHz

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

Note 2: The LTC2054/LTC2055 are designed, characterized and expected to meet the extended temperature limits of -40°C and 125°C. The LTC2054C/LTC2055C/LTC2054HVC/LTC2055HVC are guaranteed to meet the temperature limits of 0°C and 70°C. The LTC2054I/LTC2055I/LTC2054HVI/LTC2055HVI are guaranteed to meet temperature limits of -40°C and 85°C. The LTC2054H/LTC2055H and LTC2054HVH/LTC2055HVH are guaranteed to meet the temperature limits of -40°C and 125°C. The LTC2054HVL/LTC2055HVH are guaranteed to meet the temperature limits of -40°C and 125°C. The LTC2054HVL/LTC2055HVH are guaranteed to meet the temperature limits of -40°C and 125°C. The LTC2054HVH/LTC2055HVH are guaranteed to meet the temperature limits of -50°C and 125°C.

Note 3: These parameters are guaranteed by design. Thermocouple effects preclude measurements of these voltage levels during automated testing.

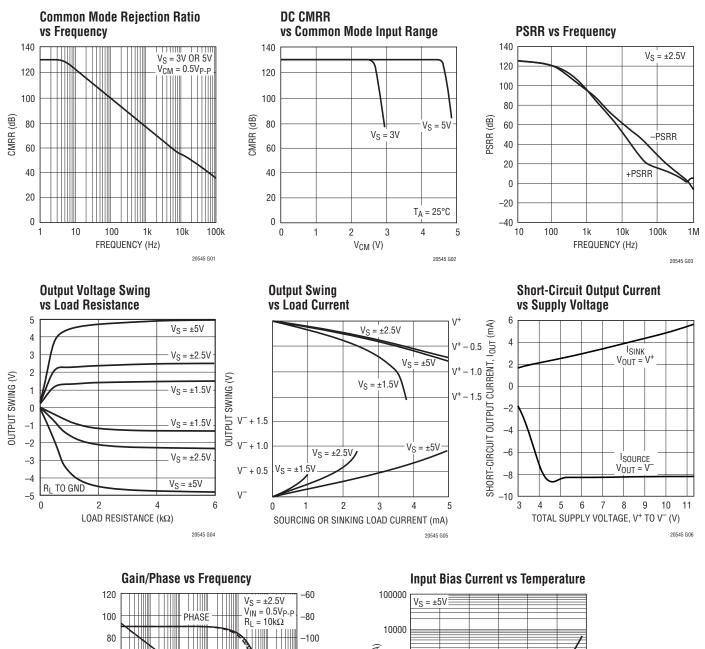
Note 4: Limit is determined by high speed automated test capability. See Typical Characteristic curves for actual typical performance. For tighter specifications, please consult Linear Technology Marketing.

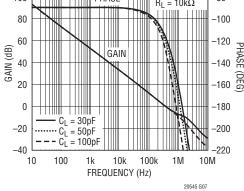
Note 5: The θ_{JA} specified for the DD package is with minimal PCB heat spreading metal. Using expanded metal area on all layers of a board reduces this value.

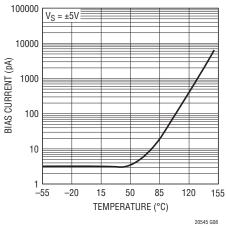




TYPICAL PERFORMANCE CHARACTERISTICS





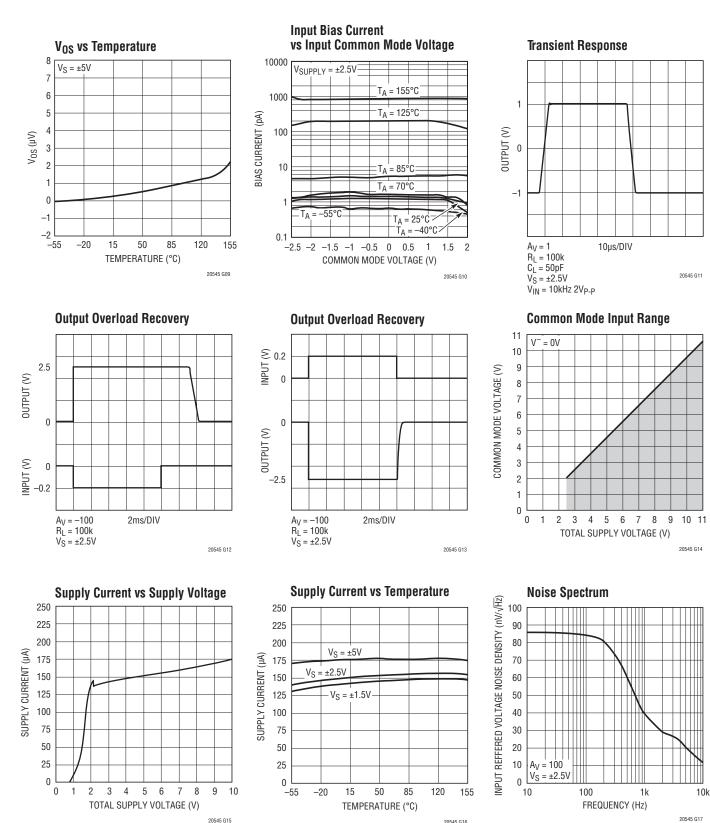




LTC2054/LTC2055

TYPICAL PERFORMANCE CHARACTERISTICS

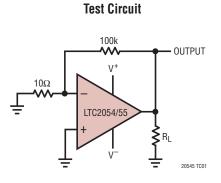
20545 G15



20545 G16

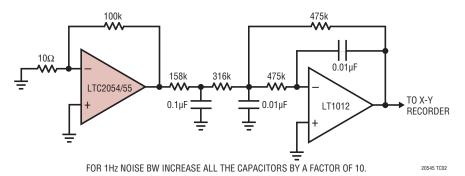


TEST CIRCUITS



Electrical Characteristics

DC-10Hz Noise Test Circuit





APPLICATIONS INFORMATION

Clock Feedthrough, Input Bias Current

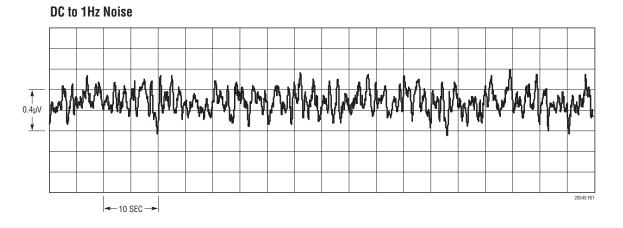
The LTC2054 and LTC2055 use auto-zeroing circuitry to achieve an almost zero DC offset over temperature, common mode voltage, and power supply voltage. The frequency of the clock used for auto-zeroing is typically 1.0kHz. The term "clock feedthrough" is broadly used to indicate visibility of this clock frequency in the op amp output spectrum. There are typically two types of clock feedthrough in auto-zeroed op amps like the LTC2054/LTC2055.

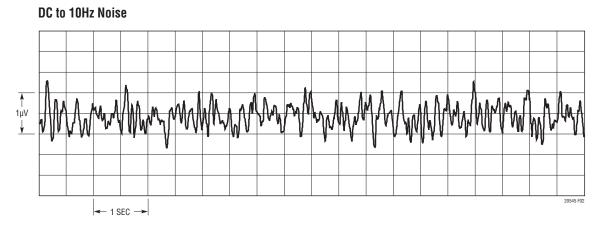
The first form of clock feedthrough is caused by the settling of the internal sampling capacitor and is input referred; that is, it is multiplied by the closed loop gain of the op amp. This form of clock feedthrough is independent of the magnitude of the input source resistance or the magnitude of the gain setting resistors. The LTC2054/LTC2055 have an input referred residue clock feedthrough of less then $0.2\mu V_{RMS}$ at 1.0kHz.

The second form of clock feedthrough is caused by the small amount of charge injection occurring during the sampling and holding of the op amp's input offset voltage. The current spikes are multiplied by the impedance seen at the input terminals of the op amp, and the resulting voltage spikes appear at the output multiplied by the closed loop gain of the op amp. To reduce this form of clock feedthrough, use smaller valued gain setting resistors and minimize the source resistance at the input. If the resistance seen at the inputs is less than 10k Ω , this form of clock feedthrough from the first form described above.

Placing a capacitor across the feedback resistor reduces either form of clock feedthrough by limiting the bandwidth of the closed loop gain.

Input bias current is defined as the DC current into the input pins of the op amp. The same current spikes that







APPLICATIONS INFORMATION

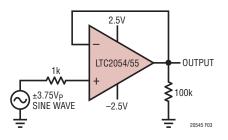
cause the second form of clock feedthrough described above, when averaged, dominate the DC input bias current of the op amp below 70°C.

At temperatures above 70°C, the leakage of the ESD protection diodes on the inputs increases the input bias currents of both inputs in the positive direction, while the current caused by the charge injection stays relatively constant. At elevated temperatures (above 70°C) the leakage current begins to dominate and both the negative and positive pins' input bias currents are in the positive direction (into the pins).

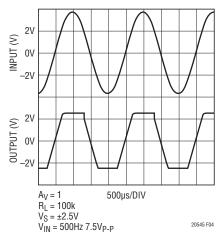
Extended Common Mode Range

The LTC2054/LTC2055 input stage is designed to allow nearly rail-to-rail input common mode signals. In addition, signals that extend beyond the allowed input common mode range do not cause output phase inversion.

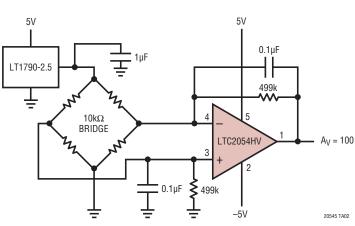
Voltage Follower with Input Exceeding the Common Mode Range







TYPICAL APPLICATIONS

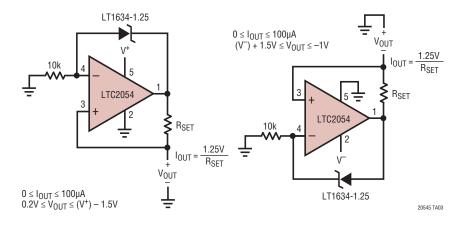


Simple Differential Bridge Amplifier

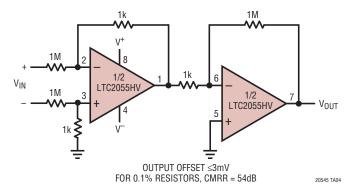


TYPICAL APPLICATIONS

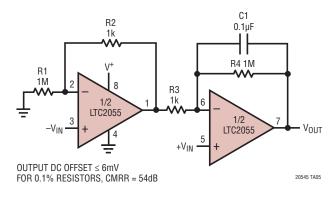
Ground Referred Precision Current Sources



Instrumentation Amplifier with 100V Common Mode Input Voltage

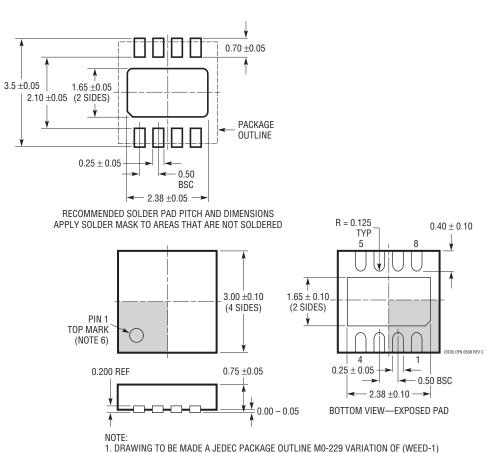


Gain of 1001 Single Supply Instrumentation Amplifier





PACKAGE DESCRIPTION



DD Package 8-Lead Plastic DFN (3mm × 3mm) (Reference LTC DWG # 05-08-1698 Rev C)

1. DRAWING TO BE MADE A JEDEC PACKAGE OUTLINE M0-229 VARIATION OF (WEED-1) 2. DRAWING NOT TO SCALE

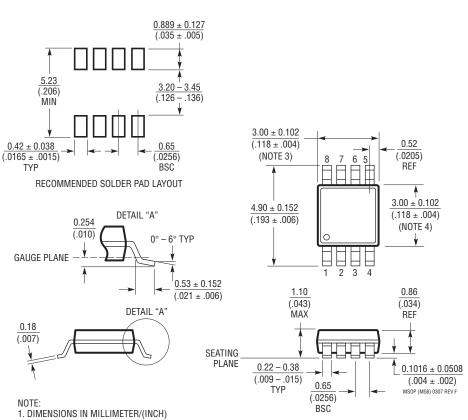
3. ALL DIMENSIONS ARE IN MILLIMETERS

- 4. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE
- MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE 5. EXPOSED PAD SHALL BE SOLDER PLATED
- 6. SHADED AREA IS ONLY A REFERENCE FOR PIN 1 LOCATION ON TOP AND BOTTOM OF PACKAGE



LTC2054/LTC2055

PACKAGE DESCRIPTION



MS8 Package 8-Lead Plastic MSOP (Reference LTC DWG # 05-08-1660 Rev F)

2. DRAWING NOT TO SCALE

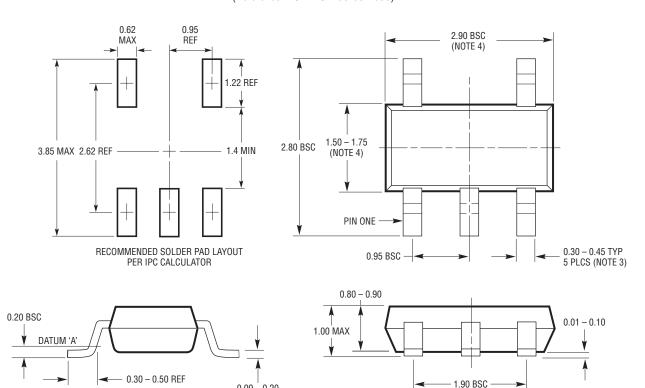
3. DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.152mm (.006") PER SIDE

- 4. DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
- INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
- 5. LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.102mm (.004") MAX



S5 TS0T-23 0302 REV B

PACKAGE DESCRIPTION



0.09 - 0.20

(NOTE 3)

S5 Package 5-Lead Plastic TSOT-23 (Reference LTC DWG # 05-08-1635)

NOTE:

1. DIMENSIONS ARE IN MILLIMETERS 2. DRAWING NOT TO SCALE

3. DIMENSIONS ARE INCLUSIVE OF PLATING

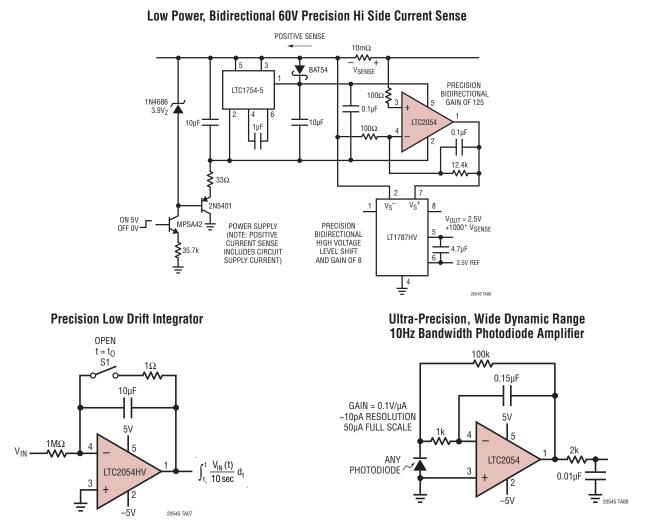
4. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH AND METAL BURR

5. MOLD FLASH SHALL NOT EXCEED 0.254mm

6. JEDEC PACKAGE REFERENCE IS MO-193



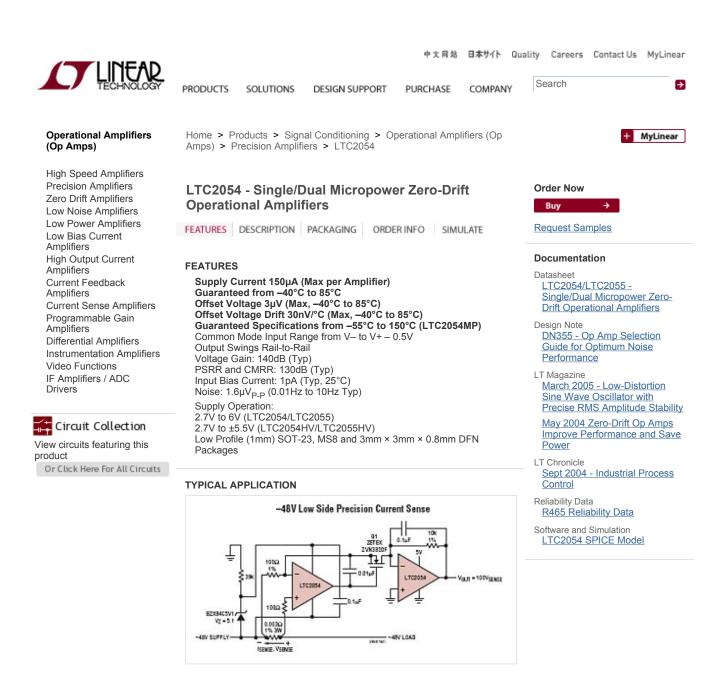
TYPICAL APPLICATIONS

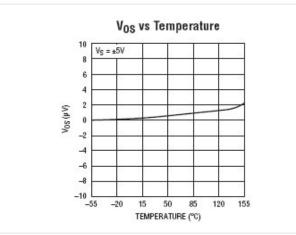


RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LTC1049	Low Power Zero-Drift Op Amp	Low Supply Current 200µA
LTC1050	Precision Zero-Drift Op Amp	Single Supply Operation 4.75V to 16V, Noise Tested and Guaranteed
LTC1051/LTC1053	Precision Zero-Drift Op Amp	Dual/Quad Version of the LTC1050
LTC1150	±15V Zero-Drift Op Amp	High Voltage Operation ±18V
LTC1152	Rail-to-Rail Input and Output Zero-Drift Op Amp	Single Zero-Drift Op Amp with Rail-to-Rail Input and Output and Shutdown
LT1677	Low Noise Rail-to-Rail Input and Output Precision Op Amp	$V_{0S} = 90\mu$ V, $V_{S} = 2.7$ V to 44V
LT1884/LT1885	Rail-to-Rail Output Precision Op Amp	$V_{0S} = 50 \mu V$, $I_B = 400 pA$, $V_S = 2.7 V$ to 40V
LTC2050	Zero-Drift Op Amp	Enhanced Output Drive Capability
LTC2051/LTC2052	Dual/Quad Zero-Drift Op Amp	Dual/Quad Version of the LTC2050 in MS8/GN16 Package
LTC2053	Zero-Drift Instrumentation Amp	Rail-to-Rail Input

LT 0809 REV C · PRINTED IN USA





DESCRIPTION

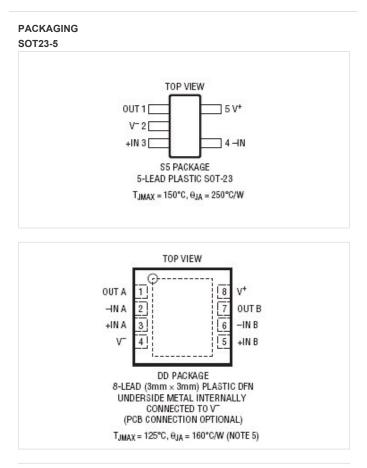
BACK TO TOP

The LTC2054/LTC2055 are low power, low noise, single/ dual, zero-drift operational amplifiers with an extended temperature version (LTC2054MP) that has guaranteed specifications from –55°C to 150°C. They are available in SOT-23 (ThinSOT) and MS8 packages. For space limited applications, the LTC2055 is also available in a 3mm × 0.8mm dual fine pitch leadless package (DFN). They operate from a single 2.7V minimum supply and support ± 5V applications. The current consumption is typically 150µA for the LTC2054 and 130µA/amp for the LTC2055.

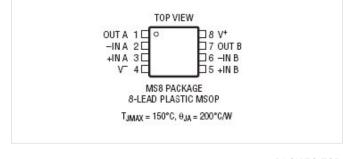
The LTC2054/LTC2055, despite their miniature size, feature uncompromising DC performance. The maximum input offset voltage and offset drift are 3.0µV and 30nV/°C over the -40°C to 85°C industrial temperature range. The almost zero DC offset and drift are supported with a power supply rejection ratio (PSRR) and common mode rejection ratio (CMRR) of 130dB (typ).

The input common mode voltage ranges from the negative supply up to typically 0.5V below the positive supply. The open-loop gain is typically 140dB. The LTC2054/LTC2055 also feature a $1.6\mu V_{P,P}$ DC to 10Hz noise and a 500kHz gain-bandwidth product.

BACK TO TOP



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BACK TO TOP

ORDER INFO

Part numbers ending in PBF are lead free. Please contact LTC marketing for information on lead based finish parts.

Part numbers containing TR or TRM are shipped in tape and reel or 500 unit mini tape and reel, respectively

Please refer to our <u>general ordering information</u> or the product datasheet for more details

Package Variations and Pricing

Part Number	Package	Pins	Temp	Price (1- 99)	Price (1k) [*]	RoHS Data
LTC2054CS5	SOT	5	С	\$1.28		View
LTC2054CS5#PBF	<u>SOT</u>	5	С	\$1.28		View
LTC2054CS5#TR	<u>SOT</u>	5	С	\$1.28	\$1.00	<u>View</u>
LTC2054CS5#TRM	<u>SOT</u>	5	С	\$1.28	\$1.02	View
LTC2054CS5#TRMPBF	<u>SOT</u>	5	С	\$1.28	\$1.02	View
LTC2054CS5#TRPBF	<u>SOT</u>	5	С	\$1.28	\$1.00	<u>View</u>
LTC2054HS5	<u>SOT</u>	5	Н	\$1.53		View
LTC2054HS5#PBF	<u>SOT</u>	5	Н	\$1.53		View
LTC2054HS5#TR	<u>SOT</u>	5	Н	\$1.53	\$1.26	View
LTC2054HS5#TRM	<u>SOT</u>	5	Н	\$1.53	\$1.28	View
LTC2054HS5#TRMPBF	<u>SOT</u>	5	Н	\$1.53	\$1.28	View
LTC2054HS5#TRPBF	<u>SOT</u>	5	Н	\$1.53	\$1.26	View
LTC2054HVCS5	<u>SOT</u>	5	С	\$1.58		View
LTC2054HVCS5#PBF	SOT	5	С	\$1.58		View
LTC2054HVCS5#TR	<u>SOT</u>	5	С	\$1.58	\$1.25	View
LTC2054HVCS5#TRM	<u>SOT</u>	5	С	\$1.58	\$1.27	View
LTC2054HVCS5#TRMPBF	SOT	5	С	\$1.58	\$1.27	View
LTC2054HVCS5#TRPBF	<u>SOT</u>	5	С	\$1.58	\$1.25	View
LTC2054HVHS5	<u>SOT</u>	5	Н	\$1.88		View
LTC2054HVHS5#PBF	<u>SOT</u>	5	Н	\$1.88		View
LTC2054HVHS5#TR	<u>SOT</u>	5	Н	\$1.88	\$1.56	View
LTC2054HVHS5#TRM	<u>SOT</u>	5	Н	\$1.88	\$1.58	View
LTC2054HVHS5#TRMPBF	<u>SOT</u>	5	Н	\$1.88	\$1.58	View
LTC2054HVHS5#TRPBF	SOT	5	Н	\$1.88	\$1.56	View
LTC2054HVIS5	<u>SOT</u>	5	Ι	\$1.88		View
LTC2054HVIS5#PBF	<u>SOT</u>	5	I	\$1.88		View
LTC2054HVIS5#TR	<u>SOT</u>	5	I	\$1.88	\$1.56	View
LTC2054HVIS5#TRM	<u>SOT</u>	5	1	\$1.88	\$1.58	View
LTC2054HVIS5#TRMPBF	<u>SOT</u>	5	I	\$1.88	\$1.58	View
LTC2054HVIS5#TRPBF	<u>SOT</u>	5	1	\$1.88	\$1.56	View
LTC2054HVMPS5#PBF	<u>SO</u>	5	MP	\$5.64		View

LTC2054HVMPS5#TRMPBF	<u>SOT</u>	5	MP	\$5.64	\$4.73	<u>View</u>
LTC2054HVMPS5#TRPBF	<u>SOT</u>	5	MP	\$5.64	\$4.68	View
LTC2054IS5	SOT	5	I	\$1.53		View
LTC2054IS5#PBF	<u>SOT</u>	5	I	\$1.53		View
LTC2054IS5#TR	<u>SOT</u>	5	I	\$1.53	\$1.26	View
LTC2054IS5#TRM	<u>SOT</u>	5	I	\$1.53	\$1.28	View
LTC2054IS5#TRMPBF	<u>SOT</u>	5	I	\$1.53	\$1.28	View
LTC2054IS5#TRPBF	<u>SOT</u>	5	I	\$1.53	\$1.26	View
LTC2054MPS5#PBF	<u>SOT</u>	5	MP	\$4.59		View
LTC2054MPS5#TRMPBF	SOT	5	MP	\$4.59	\$3.83	View
LTC2054MPS5#TRPBF	<u>SOT</u>	5	MP	\$4.59	\$3.78	View
Buy Now						
Request Samples						

The USA list pricing shown is for BUDGETARY USE ONLY, shown in United States dollars (FOB USA per unit for the stated volume), and is subject to change. International prices may differ due to local duties, taxes, fees and exchange rates. For volume-specific price or delivery quotes, please contact your local Linear Technology sales office or authorized distributor.

BACK TO TOP

APPLICATIONS

Thermocouple Amplifiers Electronic Scales Medical Instrumentation Strain Gauge Amplifiers High Resolution Data Acquisition DC Accurate RC Active Filters Low Side Current Sense Battery-Powered Systems

BACK TO TOP

SIMULATE

Linear Technology offers several options for simulating our high performance operational amplifiers.

Download the SPICE model for the LTC2054 (or right click and select "Save Target As" to save the file to disk)

Download SPICE models for the complete collection of LTC op amps

LTSpice / SwitcherCAD III is a powerful FREE circuit simulator and schematic capture program. Included in this download are LTSpice, Macro Models for 80% of Linear Technology's switching regulators, over 200 op amp models, as well as resistors, transistors and MOSFET models. <u>Download it now!</u>

BACK TO TOP